

METHOD FOR SETTING A PIXEL CLOCK OF A DISPLAY DRIVING CIRCUIT

Abstract

A method for setting a pixel clock of a display driving circuit. The display driving circuit has a basic input/output system (BIOS) for controlling a clock generator to output the pixel clock. The method includes reading a predetermined pixel clock according to a display mode setting, utilizing a plurality of scaling factors to respectively adjust a reference clock for generating a plurality of calculation results, utilizing a plurality of coefficients R to respectively right-shift R bits of the calculation results for generating a plurality of quotients, comparing a plurality of differences between the quotients and the predetermined pixel clock for obtaining an optimum quotient, and controlling the clock generator to output an actual pixel clock according to the scaling factor and the coefficient R related to the optimum quotient.